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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10001472	11/01/2001	235	296	2842	Wg

**APPLICANTS: Havemann Robert;

2815

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/251,494 12/05/2000

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐RESCIND ☐Foreign priority claimed ☐ yes ☐ no35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

TI-27506

TITLE: Integrated circuit having a doped porous dielectric and method of manufacturing the same

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-95)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Applicant Examiner	
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